



ISSUE CLASSIFICATION

PATENT NUMBER .

U.S. UTILITY Patent Application

O.I.P.E.
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PATENT DATE

APPLICATION NO. 09/775167	CONT/PRIOR F	CLASS 359	SUBCLASS 100	ART UNIT 2843 3173	EXAMINER SARAS MFL/SDJ
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Yasushi Kubota
Hajime Washio

Shift register circuit capable of reducing consumption of power with reduced capacitive load of clock signal line and image display device including it

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="text-align: center;">(Assistant Examiner)</div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<div style="text-align: center;">(Primary Examiner)</div>				
				ISSUE FEE	
	<div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">Amount Due</div> <div style="width: 45%;">Date Paid</div> </div>				
	<div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">(Legal Instruments Examiner)</div> <div style="width: 45%;">(Date)</div> </div>		ISSUE BATCH NUMBER		

The information

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